WHAT IS CLAIMED IS"

1. A GPRS replaceable module communication device, comprising a motherboard and a daughter board, wherein said motherboard has GPRS components, and said daughter board is a modularized add-on card, whose function is determined by a replaceable module, and said GPRS card determines either the control data on said motherboard, or the control data on said daughter board.

2. The device as claimed in Claim 1, wherein said GPRS component comprises at least a controlling multiplexer, an SSD device bridge chip, an EEPROM, a first bus, a second bus, a third bus, a GPRS module, and a connector.

3. The device as claimed in Claim 1, wherein said daughter board comprises at least an EEPROM, a connector, and further comprises at least one of the following three: a battery set, a memory card slot, or a Bluetooth module.

4. The device as claimed in Claim 2, wherein said control data of said motherboard is stored in said EEPROM.

5. The device as claimed in Claim 3, wherein said control data of said daughter board is stored in said EEPROM.

6. The device as claimed in Claim 5, wherein said control data is transmitted through said connector on said motherboard, said first bus, said SSD device bridge chip, said second bus, to said controlling multiplexer.

7. The device as claimed in Claim 5, wherein said data controller transmitted through said connector on said motherboard, or said third bus to said controlling multiplexer.

8. The device as claimed in Claim 6, wherein the transmission path of said control data is determined by a switch chip.

9. The device as claimed in Claim 8, wherein the operation of said switch chip is determined by the action of insertion or removal of said daughter board.

10. The device as claimed in Claim 7, wherein the transmission path of said control data is determined by a switch chip.

11. The device as claimed in Claim 10, wherein the operation of said switch chip is determined by the action of insertion or removal of said daughter board.

12. The device as claimed in Claim 4, wherein the operation of said controlling multiplexer to read either said control data on EEPROM of said motherboard, or said control data on said EEPROM on said daughter is executed by a switch chip.

13. The device as claimed in Claim 12, wherein the operation of said switch chip is determined by the turning on or off the pins of said switch chip through the action of insertion or removal of said daughter board.

14. The device as claimed in Claim 5, wherein the operation of said controlling multiplexer to read either said control data on EEPROM of said motherboard, or said control data on said EEPROM on said daughter is executed by a switch chip.

15. The device as claimed in Claim 14, wherein the operation of said switch chip is determined by the turning on or off the pins of said switch chip through the action of insertion or removal of said daughter board.